REMARKS

The present Preliminary Amendment is submitted to correct a typographical error.

Attached hereto is a marked-up version of the changes made to the specification and claims by the current Amendment. The attached page is captioned "Version With Markings to Show Changes Made".

Respectfully submitted,

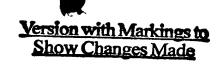
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equalization controller according to the seventh embodiment.

Figure 15 is a block diagram illustrating a structure of a waveform equalization controller according to an eighth embodiment of the present invention.

Figures 16(a)-16(b) are diagrams for explaining the operation of the waveform equalization controller according to the eighth embodiment of the present invention.

Figure 17 is a block diagram illustrating a structure of a waveform equalization controller according to a ninth embodiment of the present invention.

Figures 18(a)-18(a) are diagrams for explaining the operation of the waveform equalization controller according to the ninth embodiment of the present invention.

Figure 19 is a block diagram illustrating a structure of a prior art waveform equalization controller.

Figure 20 is a diagram illustrating a structure of a DTV signal format.

Figure 21 is a diagram illustrating a structure of a field synchronous signal format in the DTV signal.

Figures 22(a)-22(b) are diagrams for explaining the operation of the prior art waveform equalization controller.

Figure 23 is a flowchart showing the operation of the